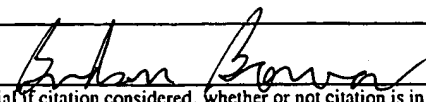


First Named Inventor	Christophe Chevallier	INFORMATION DISCLOSURE STATEMENT FORM PTO-1449
Serial No.	Unknown 10/782,247	
Filing Date	Herewith	
Group Art Unit	Unknown 2825	
Examiner Name	Unknown Bowels	
Attorney Docket No.	400.184US03	
Title: CHECKING LAYOUT ACCURACY IN INTEGRATED CIRCUIT DESIGNS		

U.S. Patent References				
Examiner Initials	Document Number	Issue/Publication Date	Name	Filing Date
BB	5,706,295	01/06/1998	Suzuki	07/26/1996
BB	5,963,729	10/05/1999	Aji et al.	06/26/1997
BB	6,038,020	03/14/2000	Tsukuda	09/22/1998
BB	6,038,383	03/14/2000	Young et al.	10/13/1997
BB	6,078,737	06/20/2000	Suzuki	09/18/1997
BB	6,115,546	09/05/2000	Chevallier et al.	07/22/1998
BB	6,295,627 B1	09/25/2001	Gowni et al.	04/02/1999
BB	6,425,113 B1	07/23/2002	Anderson et al.	06/13/2000
BB	6,470,477 B1	10/22/2002	Scott	12/23/1999
BB	6,516,451 B1	02/04/2003	Patin	07/05/2000
BB	6,546,540 B1	04/08/2003	Igarashi et al.	11/15/2000

Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	T?
	Country	No.			

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.

Examiner Signature		Date Considered	4/14/05
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			